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CONFIRMATION NO. 3942

SERIAL NUMBER 10/562,074	FILING or 371(c) DATE 04/12/2007 RULE	CLASS 324	GROUP ART UNIT 2858	ATTORNEY DOCKET NO. DE03 0214 US1		
APPLICANTS Martin Kadner, Hamburg, GERMANY; ** CONTINUING DATA ***** This application is a 371 of PCT/IB04/50927 06/17/2004 TSD ** FOREIGN APPLICATIONS ***** EUROPEAN PATENT OFFICE (EPO) 031018708 06/25/2003 TSD ** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 06/13/2007						
Foreign Priority claimed <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119(a-d) conditions met <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Verified and Acknowledged <u>/TIMOTHY J DOLE/</u> Examiner's Signature		<input type="checkbox"/> Met after Allowance TJD Initials	STATE OR COUNTRY GERMANY	SHEETS DRAWINGS 1	TOTAL CLAIMS 2	INDEPENDENT CLAIMS 2
ADDRESS NXP, B.V. NXP INTELLECTUAL PROPERTY DEPARTMENT M/S41-SJ 1109 MCKAY DRIVE SAN JOSE, CA 95131 UNITED STATES						
TITLE Method and Circuit Arrangement for the Self-Testing of a Reference Voltage in Electronic Components						
FILING FEE RECEIVED 1030	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:			<input type="checkbox"/> All Fees		
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